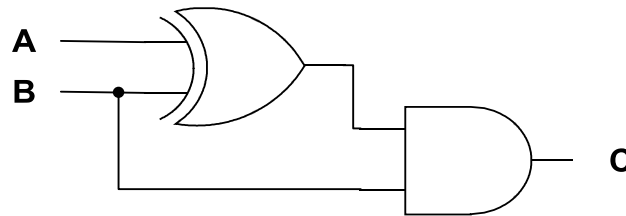


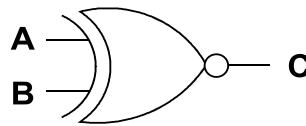
# PACE Electrical Engineering

## Digital Logic Exercises

1. Create truth tables for D using the two expressions at the end of the Digital Logic lesson handout.
2. Create the truth table for C in the following diagram



3. The XNOR gate has the symbol below. Based on what we've learned about the symbols for logic gates, determine the truth table for the XNOR gate.



4. Create a circuit diagram from the following formula:  $D = A\bar{B} + \bar{A}C$
5. Create an expression for D from the following truth table

A	B	C	D
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1